

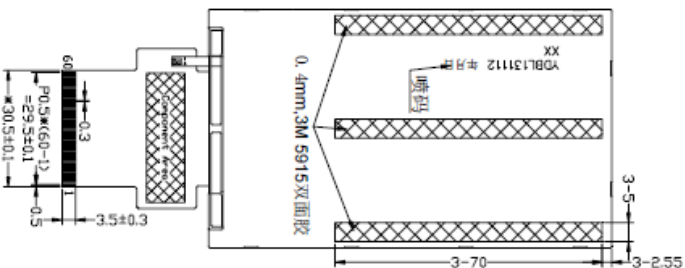
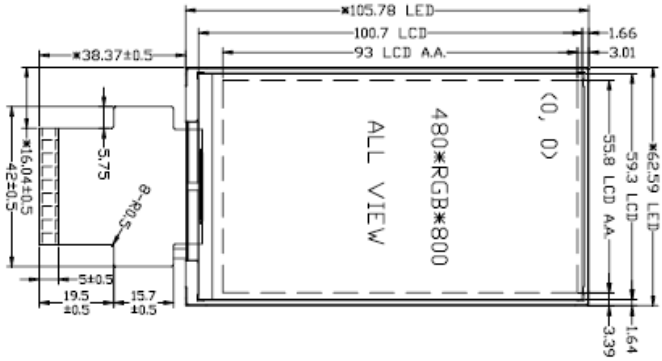


**LCD**  
MIKROELEKTRONIK

# YTS43KLAG-01-100N

Date  
08.06.2016

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Backlight circuit diagram:  
 $V_f = 1.4V(\text{min.})$   $15.5V(\text{typ.})$   $17V(\text{max.})$   
 $I_f = 40mA$



PIN	SYMBOL	PIN	SYMBOL	PIN	SYMBOL
1	GND	21	B2	41	R6
2	GND	22	D3	42	R7
3	VCC	23	B4	43	GND
4	VCC	24	B5	44	DST_D0P
5	BS0	25	B6	45	DST_D0N
6	BS1	26	B7	46	GND
7	NC	27	G0	47	DST_CN
8	NC	28	G1	48	DST_CP
9	NC	29	G2	49	GND
10	RESX	30	G3	50	DST_DIP
11	SD0	31	G4	51	DST_D1N
12	SD1	32	G5	52	GND
13	SCL	33	G6	53	NC
14	CS	34	G7	54	NC
15	DOTCLK	35	R0	55	GND
16	DE	36	R1	56	NC
17	HSYNC	37	R2	57	NC
18	VSYNC	38	R3	58	GND
19	B0	39	R4	59	A
20	B1	40	R5	60	K

1	Operating Voltage:	VCC=3.3V(typ.)
2	Resolution:	480RGB*800
3	Color:	16M
4	Interface:	24-bits RGB, MIPI
5	Display type:	Transmissive, IPS
6	Viewing Direction:	Normally black
7	Operating Temp.:	Full view
8	Storage Temp.:	-20°C~70°C
9	Driver IC:	-30°C~80°C
10	Unspecified tolerance:	HX8363-A
11	LCM Luminance:	F0.2
		340cd/m <sup>2</sup> (min.)
		480cd/m <sup>2</sup> (typ.)