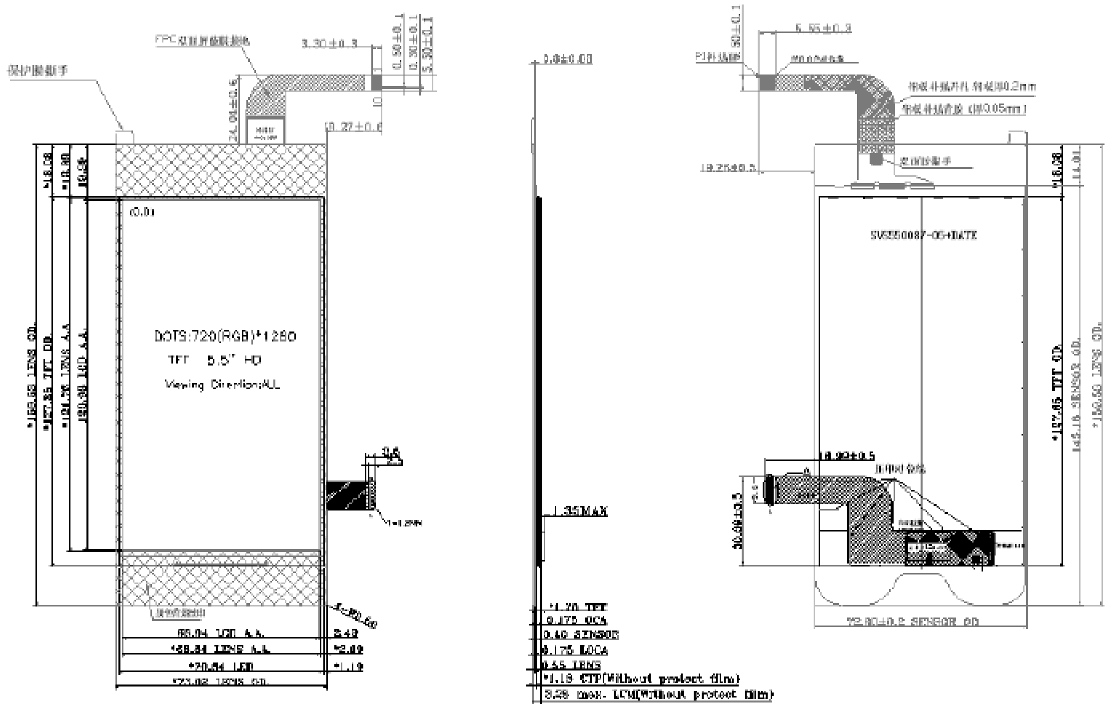
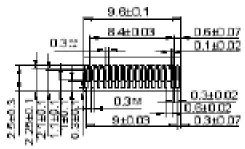


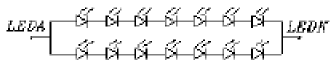




A

Backlight circuit diagram:

Vf = 21V(min.) 23.1V(max.)

If = 40mA




注:

1、TFT 12脚和4脚内部短接，13脚和5脚内部短接

2、TFT 5和6脚作为识别脚，上拉电阻接高

CTP PIN ASSIGNMENT

| | |
|---|---|
| 1 | GND |
| 2 | SD2_2_OUT_3_IN |
| 3 | GND |
| 4 | SD2_3_OUT_4_IN |
| 5 | TP_INT(3.3V) |
| 6 | TP_RST(3.3V) |
| 7 | SDA(3.3V) |
| 8 | SCL(3.3V) |
| 9 | VCC_3.3V |
| 10 | GND |

TFT PIN ASSIGNMENT

| | |
|---|---|
| 1 | GND |
| 2 | LEDK |
| 3 | LEDA |
| 4 | LCD_PWM |
| 5 | IDD([illegible] 4.7K [illegible] 1.8V) |
| 6 | LPTE([illegible] 4.7K [illegible] 1.8V) |
| 7 | RST |
| 8 | GND |
| 9 | VCCIO1.8V |
| 10 | VCC 2.8V |
| 11 | GND |
| 12 | SD7_1_IN |
| 13 | SD6_1_IN |
| 14 | SD7_1_OUT_2_IN |
| 15 | SD6_1_OUT_2_IN |
| 16 | GND |
| 17 | MIPI_D3N |
| 18 | MIPI_D3P |
| 19 | GND |
| 20 | MIPI_D0N |
| 21 | MIPI_D0P |
| 22 | GND |
| 23 | MIPI_CLKN |
| 24 | MIPI_CLKP |
| 25 | GND |
| 26 | MIPI_D1N |
| 27 | MIPI_D1P |
| 28 | GND |
| 29 | MIPI_D2N |
| 30 | MIPI_D2P |
| 31 | GND |

| | | |
|---|---|---|
| 1 | Operating Voltage: | VCC=2.8V(typ.)<br>VCC_3.3V=3.3V(typ.) |
| 2 | Resolution: | 720RGB*1280 |
| 3 | Color: | 16M |
| 4 | Interface: | MIPI |
| 5 | Display type: | Transmissive, IPS<br>Normally black |
| 6 | Viewing Direction: | ALL VIEW |
| 7 | Operating Temp: | -10°C~60°C |
| 8 | Storage Temp: | -20°C~70°C |
| 9 | Driver IC: | TFT:SC10967<br>CTP:GT970(11X19) |
| 10 | Unspecified tolerance: | ±0.2 |
| 11 | LCM Luminance: | 320 cd/m^2 (min.)<br>400 cd/m^2 (typ.) |